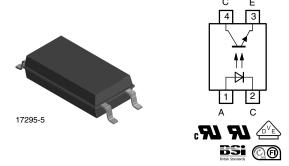




www.vishay.com

## Vishay Semiconductors

# **Optocoupler, Phototransistor Output,** SOP-4L, Long Mini-Flat Package



#### **DESCRIPTION**

The TCLT100. series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4-lead SOP4L package.

#### **APPLICATIONS**

- Switchmode power supplies
- Computer peripheral interface
- Microprocessor system interface

#### **FEATURES**

- SMD low profile 4 lead package
- V<sub>IORM</sub> = 1050 V
- CTR flexibility available see order information
- Special construction
- · Extra low coupling capacitance
- DC input with transistor output
- Creepage distance > 8 mm
- · Material categorization: For definitions of compliance please see www.vishav.com/doc?99912





RoHS COMPLIANT HALOGEN FREE

# **GREEN**

#### **AGENCY APPROVALS**

- UL1577, file no. E76222
- CSA (cUL) 22.2 bulletin 5A recognized file no. E-76222
- BSI: BS EN 41003, BS EN 60065 (BS 415), BS EN 60950 (BS 7002), certificate number 7081 and 7402
- DIN EN 60747-5-5 (VDE 0884), available with option 1
- FIMKO: EN 60950
- CQC

#### Note

See the safety standard approval list "Agency Table" for more detailed information.

ORDERING INFORMATION										
Т	С	L	Т	1	0	0	#		SOP-4L	h
PART NUMBER						<b>■</b> 10.2 mm	_			
AGENCY	AGENCY CTR (%)									
CERTIFIED/PACKAGE		5 mA		10 mA				5 mA		
UL, cUL, VDE, BSI, FIMKO 5		50 to 600	63 to 125	100 to 200	160 to 320	50 to 150	100 to 300	80 to 160	130 to 260	200 to 400
SOP-4L	•	TCLT1000	TCLT1002	TCLT1003	TCLT1004	TCLT1005	TCLT1006	TCLT1007	TCLT1008	TCLT1009

#### Note

· Available only on tape and reel.



## TCLT100. Series

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		V <sub>R</sub>	6	V				
Forward current		I <sub>F</sub>	60	mA				
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	Α				
Power dissipation		P <sub>diss</sub>	100	mW				
Junction temperature		Tj	125	°C				
OUTPUT								
Collector emitter voltage		V <sub>CEO</sub>	70	V				
Emitter collector voltage		V <sub>ECO</sub>	7	V				
Collector current		Ic	50	mA				
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA				
Power dissipation		P <sub>diss</sub>	150	mW				
Junction temperature		Tj	125	°C				
COUPLER								
Isolation test voltage (RMS)		V <sub>ISO</sub>	5000	$V_{RMS}$				
Total power dissipation		P <sub>tot</sub>	250	mW				
Operating ambient temperature range		T <sub>amb</sub>	- 55 to + 100	°C				
Storage temperature range		T <sub>stg</sub>	- 55 to + 125	°C				
Soldering temperature		T <sub>sld</sub>	260	°C				

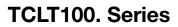
#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
INPUT									
Forward voltage	$I_F = 50 \text{ mA}$	V <sub>F</sub>		1.25	1.6	V			
Junction capacitance	$V_R = 0 V, f = 1 MHz$	C <sub>j</sub>		50		pF			
OUTPUT									
Collector emitter voltage	$I_C = 1 \text{ mA}$	V <sub>CEO</sub>	70			V			
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V			
Collector emitter cut-off current	V <sub>CE</sub> = 20 V, I <sub>F</sub> = 0 A	I <sub>CEO</sub>		10	100	nA			
COUPLER									
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V <sub>CEsat</sub>			0.3	V			
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>		110		kHz			
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.3		pF			

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.





CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)										
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT			
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCLT1000	CTR	50		600	%			
		TCLT1002	CTR	63		125	%			
	$V_{CE} = 5 \text{ V}, I_{F} = 10 \text{ mA}$	TCLT1003	CTR	100		200	%			
		TCLT1004	CTR	160		320	%			
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 1 mA	TCLT1002	CTR	22	45		%			
1 /		TCLT1003	CTR	34	70		%			
I <sub>C</sub> /I <sub>F</sub>		TCLT1004	CTR	56	100		%			
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 5 mA	TCLT1005	CTR	50		150	%			
		TCLT1006	CTR	100		300	%			
		TCLT1007	CTR	80		160	%			
		TCLT1008	CTR	130		260	%			
		TCLT1009	CTR	200		400	%			

SAFETY AND INSULATION RATED PARAMETERS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	2			kV		
Partial discharge test voltage -	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s},$	V <sub>IOTM</sub>			8	kV <sub>peak</sub>		
lot test (sample test)	(see figure 2)	V <sub>pd</sub>			1.68	kV <sub>peak</sub>		
	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω		
Insulation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	10 <sup>11</sup>			Ω		
Insulation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>			Ω		
Forward current		I <sub>si</sub>	130			mA		
Power dissipation		P <sub>so</sub>	265			mW		
Rated impulse voltage		V <sub>IOTM</sub>	8			kV		
Safety temperature		T <sub>si</sub>	150			°C		
Comparative tracking index		CTI		175				
Clearance distance			8.0			mm		
Creepage distance			8.0			mm		
Insulation distance (internal)			0.40			mm		

#### Note

• According to DIN EN 60747-5-2 (VDE 0884) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

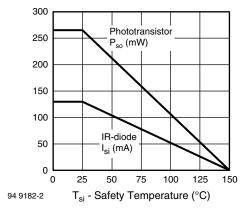


Fig. 1 - Derating Diagram

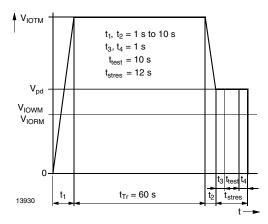


Fig. 2 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-2 (VDE 0884); IEC60747-5-5





<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Delay time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see figure 3)	t <sub>d</sub>		3		μs		
Rise time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see figure 3)	t <sub>r</sub>		3		μs		
Fall time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see figure 3)	t <sub>f</sub>		4.7		μs		
Storage time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see figure 3)	t <sub>s</sub>		0.3		μs		
Turn-on time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see figure 3)	t <sub>on</sub>		6		μs		
Turn-off time	$V_S$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , (see figure 3)	t <sub>off</sub>		5		μs		
Turn-on time	$V_S$ = 5 V, $I_F$ = 10 mA, $R_L$ = 1 k $\Omega$ , (see figure 4)	t <sub>on</sub>		9		μs		
Turn-off time	$V_S$ = 5 V, $I_F$ = 10 mA, $R_L$ = 1 k $\Omega$ , (see figure 4)	t <sub>off</sub>		10		μs		

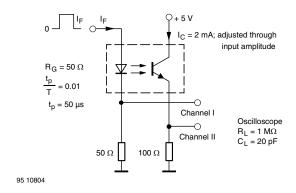


Fig. 3 - Test Circuit, Non-Saturated Operation

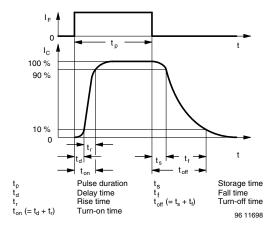


Fig. 5 - Switching Times

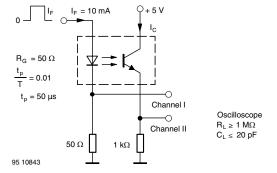


Fig. 4 - Test Circuit, Saturated Operation

#### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

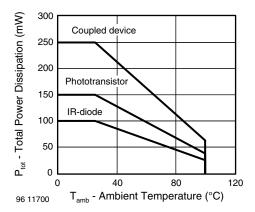


Fig. 6 - Total Power Dissipation vs. Ambient Temperature

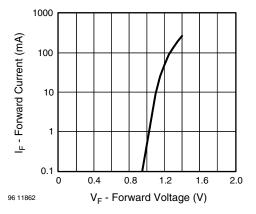


Fig. 7 - Forward Current vs. Forward Voltage

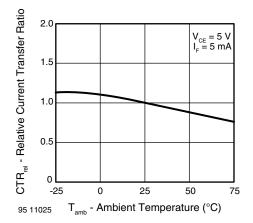


Fig. 8 - Relative Current Transfer Ratio vs. Ambient Temperature

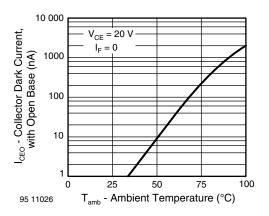


Fig. 9 - Collector Dark Current vs. Ambient Temperature

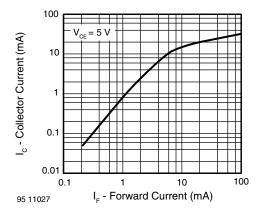


Fig. 10 - Collector Current vs. Forward Current

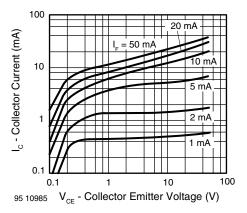


Fig. 11 - Collector Current vs. Collector Emitter Voltage



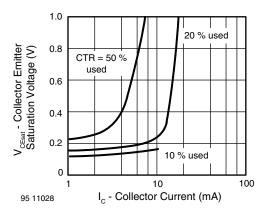


Fig. 12 - Collector Emitter Saturation Voltage vs. Collector Current

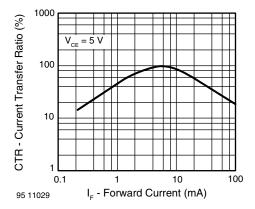


Fig. 13 - Current Transfer Ratio vs. Forward Current

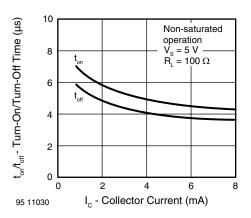


Fig. 14 - Turn-on/off Time vs. Collector Current

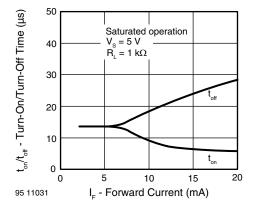
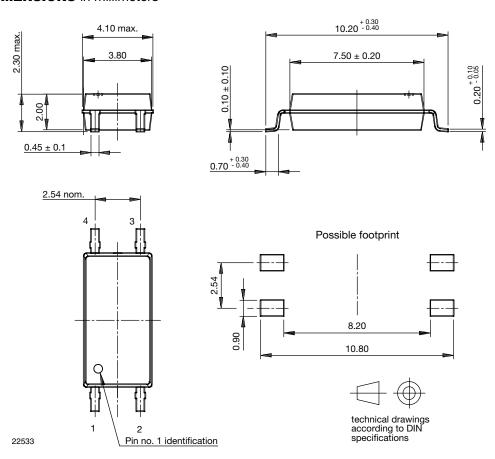


Fig. 15 - Turn-on/off Time vs. Forward Current





#### **PACKAGE DIMENSIONS** in millimeters



#### **PACKAGE MARKING (Example)**





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